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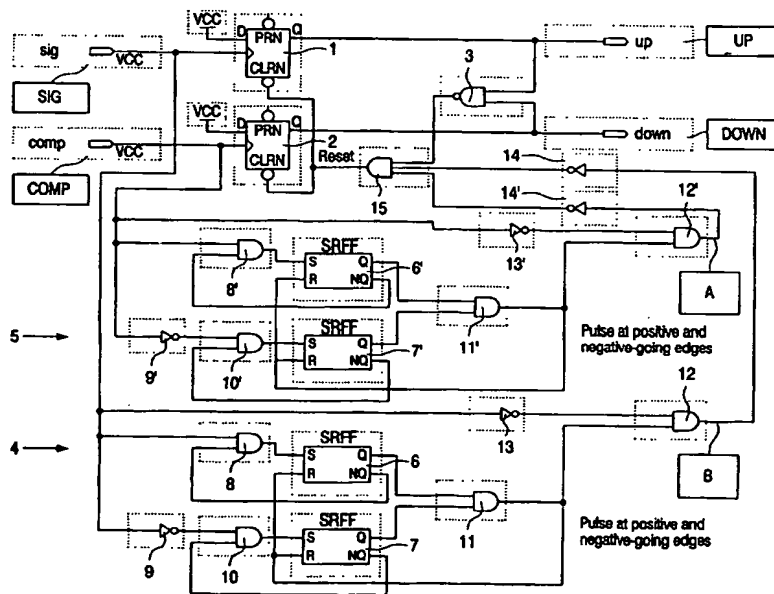
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(54) Title: PHASE COMPARATOR



(57) Abstract: A phase comparator, particularly for a PLL circuit, compares the phase angle of input signals by evaluating the edges of the input signals and generates a regulating signal. To make the regulating signal insensitive to disruptions or interference on the input signals, an additional circuit (4, 5) is provided that evaluates further, different edges of the input signals (SIG, COMP) and generates therefrom additional reset signals for the regulating signals (UP, DOWN).

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